

WHAT IS CLAIMED IS:

1. A liquid crystal display device comprising:

a liquid crystal panel including a first substrate and a second substrate, the first substrate having a plurality of source pads and gate pads, the firsts and second substrates being attached;

a first printed circuit board connected to the plurality of source pads, the first printed circuit board applying signals to the source pads;

a second printed circuit board connected to the plurality of gate pads, the second printed circuit board applying signals to the gate pads; and

a plurality of gate transmitting lines connecting the gate pads with the source pads, the plurality of gate transmitting lines transmitting signals from the first printed circuit board to the second printed circuit board via the gate transmitting lines,

wherein one of the gate transmitting lines has a resistance of below 30 Ω and is capable of transmitting a gate low voltage.

2. The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines include at least eight signal lines for transmitting signals from the first printed circuit board to the second printed circuit board.

3. The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines include common voltage signal line.

4. The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a gate high voltage signal line.

5. The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a gate low voltage signal line.

6. The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a first control signal line and a second control signal line.

7. The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a power line and a ground line.

8. The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a drive IC control signal line.

9. The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a common voltage signal line, a gate high voltage signal line, a gate low voltage signal line, first and second control signal lines, a power line and a ground line.

10. The liquid crystal display device of claim 1, further comprising a plurality of dummy pads between adjacent source pads and between adjacent gate pads.

- 18 -

11. The liquid crystal display device of claim 1, further comprising a plurality of dummy pads between adjacent gate pads.

12. The liquid crystal display device of claim 1, further comprising a plurality of dummy pads between adjacent source pads.

13. The liquid crystal display device of claim 1, wherein the plurality of gate transmitting lines are formed directly on the first substrate.

14. A liquid crystal display device comprising:

a liquid crystal panel including a substrate, the substrate having a plurality of source pads and gate pads;

a first printed circuit board connected to the plurality of source pads, the first printed circuit board applying signals to the source pads;

a second printed circuit board connected to the plurality of gate pads, the second printed circuit board applying signals to the gate pads; and

a plurality of gate transmitting lines formed directly on the substrate and connecting the gate pads with the source pads, the plurality of gate transmitting lines transmitting signals from the first printed circuit board to the second printed circuit board via the gate transmitting lines.

- 19 -

15. The liquid crystal display device according to claim 14, wherein the plurality of gate transmitting lines include at least eight signal lines for transmitting signals from the first printed circuit board to the second printed circuit board.

16. The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a gate high voltage signal line and a gate low voltage signal line.

17. The liquid crystal display device according to claim 1, wherein the plurality of gate transmitting lines includes a common voltage signal line, a gate high voltage signal line, a gate low voltage signal line, first and second control signal lines, a power line and a ground line.

18. The liquid crystal display device of claim 1, further comprising a plurality of dummy pads between adjacent gate pads.

19. The liquid crystal display device of claim 1, further comprising a plurality of dummy pads between adjacent source pads.

- 20 -

20. A method of making a liquid crystal display device comprising:

forming a liquid crystal panel including a first substrate and a second substrate, the first substrate having a plurality of source pads and gate pads, the firsts and second substrates being attached;

forming a first printed circuit board connected to the plurality of source pads, the first printed circuit board applying signals to the source pads;

forming a second printed circuit board connected to the plurality of gate pads, the second printed circuit board applying signals to the gate pads; and

forming a plurality of gate transmitting lines formed directly on the substrate and connecting the gate pads with the source pads, the plurality of gate transmitting lines transmitting signals from the first printed circuit board to the second printed circuit board via the gate transmitting lines.

21. The method according to claim 20, wherein one of the gate transmitting lines has a resistance of below 30 Ω and is capable of transmitting a gate low voltage.